Research Associate or Senior Research Associate in FPGA and Embedded Systems, with Application to Network System and Sub-systems, University of Bristol, United Kingdom.

Division/School

School of Computer Science, Electrical and Electronic Engineering and Engineering Maths

Contract type

Open Ended

Working pattern

Full time

Salary

Competitive salary starting at £33,199 per annum

Closing date for applications

30-Jun-2019

Please Note: This is a "rolling advert" with a nominal close date only. Applications are welcome at any time and the timing of the selection process will be dependent on the applications received.

The High Performance Networks group (HPN), led by Professor Dimitra Simeonidou is seeking two researchers with expertise in FPGA and embedded systems design. Researchers will carry out research in network system and sub-systems application.The High Performance Networks (HPN) group, specializes in the application and development of advanced hardware and software technologies to future optical and packet networks for 5G, data centre and smart city applications.

The HPN group features one of the largest and most illustrious optical network test-bed facilities in Europe, with additional QKD units for experimentation. National dark fibre infrastructure service (NDFIS) and UK Programmable Fixed and Mobile Internet Infrastructure (INITIATE) provides physical connections to other universities or research institutes in UK. In addition, connectivity to the Bristol 5G

City Testbed is enabled from the HPN laboratory, enabling you to design and carry out experimental research on novel FPGA based systems for deeply programmable networks.

You will carry out work leading to publications in high-quality, high-impact journals and presentations at international conferences in conjunction with internal and external research collaborations in industrial and academic laboratories in Europe and beyond.

You will carry out research, software development and experimental work for optical and high performance networks in the following fields:

Digital design of programmable optical and high performance networks

Network function programmability and virtualization

High data rate communication networks (10/100 Gbps, up to 1 Tbps)

Programmable and reconfigurable FPGA design and prototyping

Embedded system design

This appointment is offered on an open-ended contract with funding for up to 24 months in the first instance, with the possibility of extension.

*Please include your salary expectations in your application.

There is no formal closing date, but the start date for each post is flexible dependent on the circumstances of the successful candidates.

Informal enquiries can be made to: Professor Dimitra Simeonidou (Dimitra.Simeonidou@bristol.ac.uk), Dr Reza Nejabati (reza.nejabati@bristol.ac.uk) and Dr Arash Beldachi (Arash.Beldachi@bristol.ac.uk)

We appreciate and value difference, seeking to attract, develop and retain a diverse mix of talented people that will contribute to the overall success of Bristol and help maintain our position as one of the world's leading universities.